

NEWS LETTER

AY :- 2024 - 2025

ISSUE - 2

Department of
Electrical



Electronics
Engineering

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EVENTS ORGANIZED BY EEE

Report On “Two Day FDP on Design Thinking & Innovation”

Title : A Two Day Faculty Development Program on “Design Thinking & Innovation “

Event Date / Duration : 27-12-2024 & 28-12-2024 (Two days)

Resource Person : *Dr.K.Narasimha Raju (Director of CIIE & HOD-MDI &E) ,KLU,Vaddeswaram*

Names of Coordinators : Mrs.K.S.L.Lavanya ,Sr.Asst Professor
Mrs.R.Padma,Sr.Asst Professor

Target Audience : Faculty From Various Departments of LBRCE

Total no of Participants : 65 from various Departments of LBRCE.

Objective of the event :

- The objective of this FDP is to Provide training on the course “ Design thinking & innovation” among the 60+ faculty members.
- To equip faculty with the tools &methodologies of innovation &design thinking that can be integrated into teaching ,research & institution development.
- To introduce participants to design thinking process that enhance critical thinking &creative problem solving skills both in classroom &academic leadership.
- To explore innovative approaches in teaching &curriculum development focussed on experiential learning ,student ngagement & improved learning outcomes.
- To encourage faculty to apply innovative approaches strategies in their endeavors and foster stronger collaborations with industry to solve real world problems.

Outcome of event :

- Faculty got knowledge about innovative strategies for improving student learning outcomes.
- Understand about the Design thinking process
- Brain storming and development
- Encourages innovative thinking and creative problem solving.

Description/Report on event:

The EEE department has organized Two day Faculty Development program on “Design Thinking & Innovation “ in Association with Internal Quality Assurance Cell (IQAC) during 27-12-2024 &28-12-2024 .

27/12/2024: A detail discussion on key elements in design Thinking ,5 stages of Design thinking process and the Participants defined the importance of Innovation and Entrepreneurship, defined the problems associated with it and identified possible solutions.In the Afternoon session Group activities done by the participants.,Each member in the team identifies the problem .All team members discussed their observations .

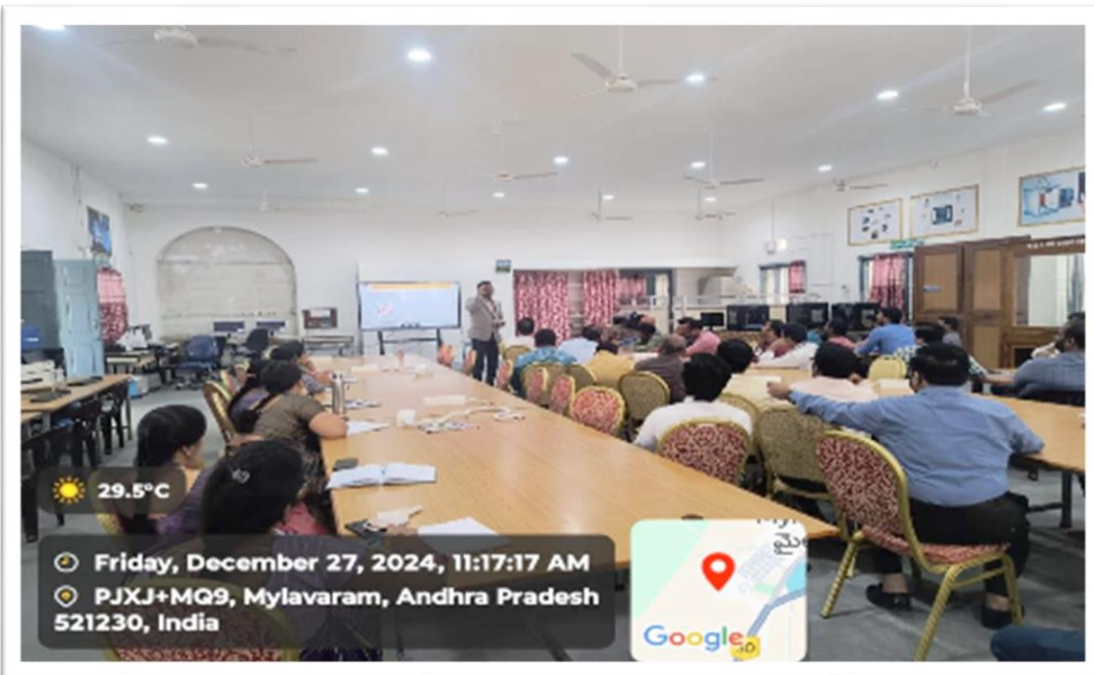
28/12/2024: A detail discussion on Goals of Empathetic Research,Power interest matrix, breakout activities done by the participants,Also discussed design process :define,customer journey map ,choosing a persona,Point of View statements .Discussed design process:Ideate,Brainstorming.design process:prototype activities done by the participants.

Finally at the end certificates are distributed to the participants by the resource person and Vice principal sir.Academic Staff felicitated the resource person.Finally participants shared feedback regarding the FDP.

Photographs:



Keynote speech by Dr.K.Apparao -Principal



FDP Session by Speaker Dr.K.Narasimharaja,Director-CIIE

Participants Activities:









FDP Circular:



LAKIREDDY BALI REDDY COLLEGE OF ENGINEERING

(An Autonomous Institution since 2010)

Approved by AICTE, New Delhi and Permanently Affiliated to JNTUK, Kakinada

L.B. Reddy Nagar, Mylavaram, NTR District, Andhra Pradesh - 521230

Dt:19-12-2024

CIRCULAR

We are delighted to announce that the EEE Department and IQAC jointly organizing Two - day offline Faculty Development Program on "Design Thinking and Innovation" during 27-12-2024 to 28-12-2024.

We request the support of all department HoDs to encourage and assist faculty to participate this Fdp and request to depute Design Thinking and Innovation course handling faculty of all sectionsd and also send the faculty who are free from class work to attend this fdp.The Certificates will be presented to all participants in recognition of their contribution.

Details of FDP

Title of the FDP	Design Thinking and Innovation
Mode	Offline
Dates of FDP	27-12-2024 to 28-12-2024
Resource Person	Dr.Kuthuri Narasimha Raju, Professor Department of EEE ,KLU

Note: All HOD'S please send the faculty list (the format is attached) to iqac@lbrce.ac.in by on or before 21-12-2024.

S.P. Pradeep
HOD/EEE

[Signature]
IQAC Coordinator

[Signature]
Principal



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L.B. Reddy Nagar, Mylavaram, NTR District, Andhra Pradesh - 521230

24-12-2024

Two-day FDP on “Design Thinking & Innovation” organized by department of EEE in association with IQAC on 27th & 28th, December, 2024.

Department wise Registered Count

Name of the Dept	No of Deputed faculty
AI&DS	4
ASE	2
CE	4
CSE	6
CSM	6
ECE	10
EEE	9
IT	6
ME	5
MBA	7
FED	03
TOTAL	62

Registered Faculty list

S.NO	Dept.	Name of the faculty	Designation
1.	AI&DS	Dr.Ch. Rajendra Babu	Professor
2.	AI&DS	Dr. D. Srinivasa Rao	Associate Professor
3.	AI&DS	Mrs. K. Lakshmi Padmavathi	Asst. Prof
4.	AI&DS	Mr V. Chandra Kumar	Sr.Asst. Prof
5.	ASE	Dr.A.Revanth Reddy	Associate Professor
6.	ASE	Dr. Sreenadh Chevula	Associate Professor
7.	CE	Dr.V.Rama Krishna	Professor
8.	CE	Dr.K.V.Ramana	Assoc. Professor
9.	CE	Mr.J. Rangaiah	Assoc. Professor
10.	CE	Mr.K. Harish	Sr. Asst. Professor
11.	CSE	Dr. J. Nageswara Rao	Assoc. Professor
12.	CSE	Dr. P. Bhagath	Professor
13.	CSE	Dr.D. Venkata Subbaiah	Professor
14.	CSE	Dr. B. Siva Rama Krishna	Assoc. Professor
15.	CSE	Dr. Ch. Venkata Narayana	Professor
16.	CSE	Dr. G.V. Suresh	Assoc. Professor
17.	CSM	Dr. SILAIK. SALMA ASIYA BEGUM	Assoc.Prof
18.	CSM	Mr. Y. KRANTHI KUMAR	Asst Prof

19.	CSM	Ms. P.PADMINI	Asst Prof
20.	CSM	Mrs. K.V. SAILAVANYA	Asst Prof
21.	CSM	Dr. B. RAJENDRA PRASAD	Assoc. Prof
22.	CSM	Mr. CH.JOHN WESILY	Asst. Prof
23.	ECE	Dr. M. V Sudhakar	Professor
24.	ECE	Dr. B. Rambabu	Professor
25.	ECE	Dr. T.Satyanarayana	Professor
26.	ECE	Dr. A.Narendra Babu	Professor
27.	ECE	Dr.P.Rakesh Kumar	Associate Professor
28.	ECE	Mr. V. V.Rama Krishna	Associate Professor
29.	ECE	Ms.B.Rajeswari	Sr.Assistant Professor
30.	ECE	Mr.P.James Vijay	Assistant Professor
31.	ECE	Ms.B.Lakshmi Thirupathamma	Assistant Professor
32.	ECE	Dr. B Y V N R Sivakumar	Assoc. Professor
33.	EEE	Mrs.K.S.L.Lavanya	Sr.Assistant Professor
34.	EEE	Mrs.R.Padma	Sr.Assistant Professor
35.	EEE	Dr. A.V.G.A.Marthanada	Associate Professor
36.	EEE	Dr. B.Pangedaiah	Associate Professor
37.	EEE	Mr. R. Anjaneyulu Naik	Associate Professor
38.	EEE	Mr. P.Srihari	Sr.Assistant Professor
39.	EEE	Mr. K.Nagalinga chary	Sr.Assistant Professor
40.	EEE	Mr.A.V.Ravi Kumar	Sr.Assistant Professor
41.	EEE	Mr. P.Rathnakar Kumar	Assistant Professor
42.	IT	Mrs J.GeethaRemka	Assistant Professor
43.	IT	Dr.B.Srinivasa Rao	Professor, HOD
44.	IT	Mr. G.Rajendra	Sr. Assistant Professor
45.	IT	Mrs D.VijayaSri	Sr. Assistant Professor
46.	IT	Mrs R.Pavithra	Assistant Professor
47.	IT	Mr. Ravindra Chanti Babu	Assistant Professor
48.	ME	Dr K Murahai	Associate Professor
49.	ME	Dr B Sudheer Kumar	Asst. Professor
50.	ME	Mr V Sankar Rao	Sr.Asst. Professor
51.	ME	Mr K V Viswanadh	Sr Asst. Professor
52.	ME	Ms P Mounica	Asst. Professor
53.	MBA	Dr. T.Rajasekhar	Professor
54.	MBA	Dr.V.V.Narsi Reddy	Associate Professor
55.	MBA	Dr.D.Venkateswarlu	Associate Professor
56.	MBA	Dr.K.Deepika	Associate Professor
57.	MBA	Dr.O.Naresh	Assistant Professor
58.	MBA	Dr.R. Jeyalakshmi	Assistant Professor
59.	MBA	Mr.M.S. Chakravarthy	Assistant Professor
60.	FED	Dr. N. Aruna	Sr. Assistant Professor
61.	FED	Dr. T. Radha rani	Sr. Assistant Professor
62.	FED	Mrs. Ch. Padma	Assistant Professor

Banner:



Hearty Welcome to
Resource Person and Participants

Two Day Faculty Development Program on
“Design Thinking and Innovation”
(27th & 28th December, 2024)

Resource Person :
Dr.K.Narasimha Raju
Director-CIE &HOD-MDI&E
KL University, Vaddeswaram.

Organized by
Department of EEE in association with IQAC

LAKIREDDY BALI REDDY COLLEGE OF ENGINEERING
(An Autonomous Institution Since 2010)
Approved by AICTE New Delhi and Permanently Affiliated to JNTUK, Kakinada
Accredited by NAAC 'A' & ISO: 21001:2018, 50001:2018, 14001:2015 Certified
Department of Electrical and Electronics Engineering
Accredited by NBA under Tier - 1

Invitation:

INVITATION
LAKIREDDY BALI REDDY COLLEGE OF ENGINEERING (AUTONOMOUS)
L.B.REDDY NAGAR, MYLAVARAM-521230
N.T.R (Dist), Andhra Pradesh



Cordially invites you to the
INAUGURAL FUNCTION OF
Two-Day Faculty Development Program on
“Design Thinking & Innovation”
in association with Internal Quality Assurance cell (IQAC).
(27 December-28 December, 2024)

Date: 27-12-2024, Time: 9.30 AM
Venue: EEE Seminar Hall

Resource Person: **Dr.K.Narasimha Raju**
Director-CIE &HOD-MDI&E
KL University

Chief Patrons
Sri Lakireddy JayaPrakash Reddy
Honorary Chairman, LBRCE

Sri Lakireddy Prasad Reddy
Chairman, LBRCE

Sri Lakireddy Vijay Kumar Reddy
Vice Chairman, LBRCE

Patrons
Sri.G.Srinivasa Reddy
President, LBRCE

Dr. K. Appa Rao
Principal, LBRCE

Dr.B. Ramesh Reddy
Vice-Principal, LBRCE

Dr. M. Srinivasa Rao
Dean Academics, LBRCE

Will grace the occasion



Dr. J. Siva Vara Prasad
HOD, EEE

Dr.K.R.L. Prasad
IQAC coordinator

Coordinators:
Mrs.K.S.L.Lavanya
Sr.Assst. Professor, EEE
Mrs. R. Padma
Sr.Assst. Professor, EEE

Brochure:

<p>Resource Person:</p>  <p>Dr. Kuthuri Naradimha Raju Director-CEE & IOD-MD&E KL University</p> <p>Registration Fee: Registration Fee: Free</p> <p>Target Audience: All faculty of LBRCE/PG Students</p> <p>Important Date: Duration of the Programme: Ten Days (27th - 28th) December, 2024</p> <p>Certificate Criteria:</p> <ul style="list-style-type: none">All eligible candidates will be given certificates.Attendance is mandatory <p>For more details contact: Mrs. K.S.L. Lavanya +91-9703494046 Sr. Assistant Professor Mrs. R. Padma +91-8465806389 Sr. Assistant Professor Department of EEE, Lakireddy Balla Reddy College of Engineering (Autonomous) EMAIL: kallavanu4@gmail.com padma@lbrce.ac.in</p>	<p>Committee Members</p> <p>Chief Patrons :</p> <p>Sri. Lakireddy Jaya Prakash Reddy, Honorary Chairman, IIRCEI(USA) Sri. Lakireddy Prasad Reddy Chairman, LBRCE (USA) Sri. Lakireddy Vijay Kumar Reddy Vice-Chairman, LBRCE</p> <p>Patrons:</p> <p>Sri G. Srinivasa Reddy President, LBRCE & IIRCEI Dr. K. Appa Rao Principal, LBRCE Dr. R. Ramesh Reddy Vice-Principal, LBRCE Dr. M. Srinivasa Rao Dean Academics, LBRCE Dr. K.R.L. Prasad IQAC coordinator</p> <p>Convener:</p> <p>Dr. J. Siva Vara Prasad Professor & HOD, EEE</p> <p>Coordinators: Mrs. K.S.L. Lavanya, Sr. Assistant Professor Mrs. R. Padma, Sr. Assistant Professor</p> <p>Organizing Committee: All the Faculty Members of Department of EEE</p>	<p>TWO-DAY OFFLINE FACULTY DEVELOPMENT PROGRAM ON "Design Thinking & Innovation" in association with Internal Quality Assurance Cell (IQAC) of LBRCE (27th - 28th December, 2024)</p>    <p>Organized by DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING IN ASSOCIATION WITH IQAC LAKIREDDY BALLAREDDY COLLEGE OF ENGINEERING (Autonomous)</p>  <p>Accredited by NAAC & NBA (ASE, CE, CSE, IT, ECE, EEE & ME) ISO 9001:2015 Certified Institution. Approved by AICTE, New Delhi and Affiliated to JNTU, Kakinada L.B. REDDY NAGAR, NYLAVARAM, KRISHNA DIST., A.P. 571252</p>
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<p>About the Institute:</p>  <p>LBRCE was founded through Lakireddy Balla Reddy charitable trust in 1998 which stands for quality technical education that is exemplified by the continuous strides taken towards excellence in the last two decades. LBRCE started with an intake of 100 and is reached to the current intake of 1164. UGC has accorded Autonomous Status in the year 2010, subsequently renewed in 2016, valid up to 2023. LBRCE has been accredited by NAAC with Grade 'A' and NBA (ECE, IT, CSE, EEE & MECH) under Tier-I. The College has also been awarded 2(F) and 12(B) status, apart from the recognition as a 'College with Potential for Excellence (CPE)' status from the UGC. Our institute has pride to have large pool of well-qualified and experienced faculty.</p>	<p>About the Department:</p>  <p>The Department of Electrical and Electronics Engineering offers an undergraduate program in Electrical & Electronics Engineering and a Post-graduate program in the specialization of Power Electronics & Drives. The Department has well-qualified faculty and good laboratory facilities. EEE Department is recognized as research center by JNTU, Kakinada. Department has licensed software: MATLAB (7.0), MATLAB(R2016a), PSCAD/EMTDC, LABVIEW, ETAP, MULTISIM, ANSYS, Xilinx and DIGSILENT. The department regularly conducts Guest Lectures / Seminars / Workshops / Technical paper contests for the benefit of both faculty and student community.</p> <p>About the FDP:</p> <p>One can understand Design Thinking as a method to solve problems using a process that is structured and systematic. It is one of the most effective ways to create something new. A</p>	<p>process that first understands users, identifies and analyses a problem or need and researches relevant information, after which ideas are explored and analyzed until an appropriate innovative solution to the problem need is arrived at. Hence Design Thinking could be viewed as the process that translates an idea into a blueprint for something useful, whether it's a vehicle, a building, a graphic, a service or a system.</p> <p>FDP Objectives:</p> <p>The main goal of this FDP is</p> <ul style="list-style-type: none">To learn the fundamentals/essentials of the Design thinking and innovation process.Identify Problems and be able to find solutions and apply Design thinking processes and methods to solve various problems. <p>Topics to be covered</p> <ul style="list-style-type: none">Design Thinking & Innovation ProcessDesign Thinking as a strategy for InnovationTools for Design Thinking & InnovationIdeation, Exploration and Creativity TechniquesBrain storming, Activity mappingSoft and hard prototyping
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Press Report:

Date:27-12-2024

యల్.బి.ఆర్.సి.ఈ లో ఈ ఈ ఈ విభాగం లో డిజైన్ తింకింగ్ మరియు ఇన్నోవేషన్ అనే అంశం పై ఫాకల్టీ డెవలప్మెంట్ ప్రోగ్రాం

డిజైన్ తింకింగ్ మరియు ఇన్నోవేషన్ పై సరైన అవగాహనా పెంచుకొని కెరియర్ ను అభివృద్ధి చేసుకోవాలని కె.ఎల్.యూ వడ్డేశ్వరం ప్రోఫెసర్ డా.కె.నరసింహ రాజు గారు పేర్కొన్నారు.

స్థానిక లకిరెడ్డి బాలిరెడ్డి ఇంజనీరింగ్ కళాశాలలో ఈ ఈ ఈ విభాగం మరియు ఐ.క్యూ.ఎసి వారి ఆధ్వర్యంలో “డిజైన్ తింకింగ్ మరియు ఇన్నోవేషన్” అనే అంశం పై శుక్రవారం ఫాకల్టీ డెవలప్మెంట్ ప్రోగ్రాం ప్రారంభించారు.

ఈ ప్రోగ్రాంకు రిసోర్స్ పర్సన్ గా ప్రోఫెసర్ డా.కె.నరసింహ రాజు డైరెక్టర్ అఫ్ సి.ఐ.ఐ.ఇ (కె.ఎల్.యూ వడ్డేశ్వరం) హాజరు అయ్యారు ఈ సందర్భంగా ఆయన అధ్యాపకులను ఉద్దేశించి మాట్లాడుతూ డిజైన్ తింకింగ్ మరియు ఇన్నోవేషన్ గురించి విపులంగా వివరించారు.

ఈ నెల 27 వ తేదీ మరియు 28 వ తేదీ 2 రోజుల ప్రోగ్రాం జరుగుతుందని కళాశాల ప్రెసిసిపాల్ డా.కె.అప్పారావు గారు తెలిపారు.

డిజైన్ తింకింగ్ మరియు ఇన్నోవేషన్ లో ఎప్పటికప్పుడు ఎలా ఐడియాస్ ను ఇంప్లిమెంట్ చేయాలి ప్రాటోటైప్ ని డెవలప్ చేయడం గురించి తెలుసుకుంటే కెరియర్ కు దోహద పడుతుంది అన్నారు . ఈ ప్రోగ్రాంకు రియల్ వరల్డ్ ప్రాబ్లెమ్ సొల్యూషన్స్ కు క్రిటికల్ థింకింగ్ కు ఈ ప్రోగ్రాం ఉపయోగపడుతుందని ఈ ఈ ఈ విభాగాధిపతి డా. జె.శివవర ప్రసాద్ తెలియజేశారు

ఈ కార్యక్రమానికి ఈ ఈ ఈ విభాగాధిపతి డా. జె.శివ వర ప్రసాద్ కన్వీనర్ గాను

కె.యస్ యల్ .లావణ్య , ఆర్ .పద్మ కో ఆర్డినేటర్స్ గా వ్యవహరించారు.

ఈ కార్యక్రమములో కళాశాల డీన్,వివిధ విభాగాధిపతులు 65 మంది వివిధ బ్రాంచ్ అధ్యాపకులు ఐ.క్యూ.ఎసి విభాగాధిపతి డా.కె.ఆర్.ఎల్. ప్రసాద్ గారు పాల్గొన్నారు.



Cordinators
Mrs.K.S.L.Lavanya,Sr.Asst Professor
Mrs.R.Padma,Sr.Asst Professor

HOD/EEE
Dr.J.Siva Vara Prasad

Report on Guest Lecture on “Career in VLSI & Embedded systems”

Event Type : Guest Lecture

Date / Duration : 20-03-2025

Resource Persons : Mr.Vaibhav Waghmare, VLSIGuru, Bangalore

Names of Coordinators : Mr.P.Deepak Reddy

Target Audience : B.Tech - III Year - EEE

Total no of Participants : 139 students

Objective of the event : Embedded software engineers can find career opportunities across various sectors, including medical devices, automotive, aerospace, and consumer electronics

Outcome of event :

• VLSI:

The demand for VLSI professionals, particularly in chip design and semiconductor technology, is high and expected to continue growing.

• Embedded Systems:

Embedded systems, which involve designing software and hardware for specific devices, are also in high demand, especially with the rise of IoT, autonomous systems, and other emerging technologies.

Description / Report on Event:

A career in VLSI and Embedded Systems offers numerous opportunities in a variety of industries, from consumer electronics to healthcare and automotive. With the increasing integration of smart devices and IoT technologies, professionals in these fields are in high demand. To succeed in this domain, individuals must develop a strong foundation in hardware design, programming, and system-level thinking, combined with an understanding of emerging technologies. As innovation continues, the future for professionals in VLSI and Embedded Systems remains bright, with a wealth of potential for career growth and development.

Photographs:



Addressing by Resource person



Participants



Addressing by Mr. Vaibhav

Participants

Coordinator:

Mr.P.Deepak Reddy, Associate Professor

Report on “One week workshop on Embedded IOT & Industrial Automation-Phase-II”

Event Type : One week workshop

Event Date / Duration : 03-03-2025 to 08-03-2025 (one week)

Resource Person : Dr. S. Madhavapandian, Director, Embedded, Taras Systems & Solutions in association with Tessolve Semiconductor Private Ltd.

Names of Coordinators : Dr. Y. Raghuvamsi & Mrs. K. S. L. Lavanya

Target Audience : III B. Tech Students of EEE Department.

Total no of Participants : 70

Objective of the event : The objective of this workshop is to gain the basic skills on advanced embedded system devices and controllers and configuring them using GPIO, HAL and python programming.

Outcome of event:

- Students got knowledge on different embedded system devices such as ARM controller, STM32 board, UART, I2C, MQTT, NVIC controller.
- Students have learned the programming skills such as GPIO and HAL programming, python programming using different packages, configuring devices with Wi-Fi, HTTP, ADC, DAC.

Description/Report on event:

The EEE department has organized one week workshop in association with Tessolve Semiconductor

Private Limited, Bengaluru on “Embedded IOT & Industrial Automation-Phase-II” from 03-03-2025

to 08-03-2025. The students are practiced with UART, MQTT, STM32 and acquired HAL and python

programming skills-

03/03/2025: First day, they learned about embedded system applications, ARM architecture, CC3200,

GPIO programming, UART, I2C, and practiced different tasks such as configuring Wi-Fi, serial communication using UART and serial communication between two microcontrollers using I2C.

04/03/2025: Second day, they got knowledge about SPI, CAN and USB and multitasking and RTOS.

Also, the tasks like uploading data and control to cloud using HTTP are performed.

05/03/2025: Third day, they got knowledge about MQTT, publish and subscribe and performed the

task of connection to cloud using MQTT.

06/03/2025: Students learned about STM32 and STMCube IDE and multitasking and RTOS. Also, the

basics of HAL programming and GPIO are learned and configured.

07/03/2025: Students understand about HAL interrupts-NVIC controller, enabling interrupt, interrupt

priority levels and they configured the serial communication, ADC, and DAC.**08/03/2025:** On final day, the students learned about python and AI basics and worked with different python packages and finally a project is demonstrated. In the last session, the assessment test is conducted for the students.

Photographs:



Addressing by Dr. J. Siva Vara Prasad, HOD, EEE Department



Addressing by Resource person Dr. S. Madhavapandian



Interaction with students by resource person



Workshop lecture by resource person



Valedictory speech by Resource person Dr. S. Madhavapandian



Memento presentation to resource person by Dr. J. Siva Vara Prasad, HOD, EEE Department

<p>Resource Person Dr. S. Madhusoodhan, Director, Sun Tera Systems & Solutions Bangalore.</p> <p>Registration Fee: Registration Fee Rs.2150/- per participant</p> <p>Target Audience: EEE V Sem Students</p> <p>Important Date: Last date of receiving applications for participation is 03rd March, 2025. Duration of the Program: One Week (03rd - 09th March, 2025)</p> <p>Certificate Criteria:</p> <ul style="list-style-type: none"> All eligible candidate will be given a certificate Attendance is mandatory <p>For more details contact:</p> <ol style="list-style-type: none"> Dr. Y. Raghuvamsi, +918034701744, Sr. Assistant Professor Mrs. K.S.L.Lavanya, +91 9301404036, Sr. Assistant Professor <p>Department of EEE, Labreddy Dal Reddy College of Engineering (Autonomous) labreddy24@gmail.com</p>	<p>Committee Members</p> <p>Chief Patron Sri. L. Jaya Prakash Reddy, Honorary Chairman Sri. L.R.N.K. Prasad Reddy, Chairman Sri. L. Vijaya Kumar Reddy, Vice Chairman</p> <p>Patrons Sri G. Madhusai Reddy, President Dr. K. Appa Rao, Principal Dr. B. Hanuman Reddy, Vice-Principal Dr. M. Srikrishna Rao, Dean Academics Dr. S. Pich Reddy, Professor, Dean R&D</p> <p>Convener Dr. J. Siva Vara Prasad, Professor & HOD</p> <p>Coordinators Dr. Y. Raghuvamsi, Sr. Assistant Professor Mrs. K.S.L. Lavanya, Sr. Assistant Professor</p> 	<p>ONE WEEK WORKSHOP ON</p> <p>"Embedded IoT & Industrial Automation-Phase-II" Tera System & Solutions in association with TeraSoft Semiconductors Private Ltd & EEE student branch of LDRCE (03rd - 09th March, 2025)</p>    <p>Organized by Department of ELECTRICAL & ELECTRONICS Engineering, LABREDDY DAL REDDY COLLEGE OF ENGINEERING (Autonomous)</p>  <p>Approved by UGC & AICTE, Affiliated to JNTU, Hyderabad Approved by AICTE, UGC and All India Council for Technical Education Approved by AICTE, UGC and All India Council for Technical Education Approved by AICTE, UGC and All India Council for Technical Education Approved by AICTE, UGC and All India Council for Technical Education</p>
<p>About the Institute: LDRCE was founded through Labreddy Dal Reddy charitable trust in 1998 which stands for quality technical education that is energized by the continuous strides taken towards excellence in the last two decades. LDRCE started with an intake of 100 and is reached to the current intake of 1200. UGC has accorded Autonomous Status to the year 2010. LDRCE has been accredited by NAAC with Grade 'B' and MBA (ASS, CE, CSE, IT, BDL, BBA & BMS) under Tier-2. The College has also been awarded 201 and 1201 status, apart from the recognition as a 'College with Potential for Excellence (CPE)' status from the UGC. Our institute has pride to have large pool of well-qualified and experienced faculty.</p> <p>About the Department: The Department of Electrical and Electronics Engineering offers an undergraduate program in Electrical & Electronics Engineering and a Post-graduate program in the specializations of Power Electronics & Drives. The Department has well</p>	<p>qualified faculty and good laboratory facilities. EEE Department is recognized as research centre by IITK, Kolkata. Department has licensed software: MATLAB, C++, MATLAB/SIMULINK, PSCAD/EMTDC, LABVIEW, ETAP, MULTISIM, ANSYS, Solid and CATIA/DMU. The department regularly conducts Guest Lectures / Seminars / Workshops / Technical paper sessions for the benefit of both faculty and student community.</p> <p>About the Program: Embedded IoT & Industrial Automation is playing the most significant role today covering every industry in the market. It is essential to know about the Embedded devices while learning the IoT. The embedded devices are the objects that work for various computing systems. These systems may or may not connect to the internet. The embedded system that runs the devices for the operating system is based on the language platform, where the real-time operations would be performed. Manufacturers build embedded software in electronics, eg, cars, telephones, medical appliances, etc. Keywords: ARM, STM32, IoT, MQTT, HAL, C/C++, and Python programming.</p>	<p>Objectives of the Training Program: The main goal of the workshop is to help</p> <ul style="list-style-type: none"> Students to learn, understand, and practice programs and IoT devices Conceptualization and implementation of Embedded IoT & Industrial Automation, techniques. <p>Topics to be covered:</p> <ul style="list-style-type: none"> Introduction to ARM & STM32 controllers Embedded systems and their applications Serial communication - UART, I2C SD, MQTT, NB-IOT and configuration to cloud HAL, GPIO Programming and configuration HAL interrupts, NVIC controller, DAC, ADC interfacing programs Python packages and libraries <p>Learning Objectives:</p> <ul style="list-style-type: none"> To acquire programming skills To acquire Object Oriented Software Python To develop the skill of designing Interfacing various IoT devices To develop the ability to write coding applications in IoT

Coordinators

Dr. Y. Raghuvamsi, Sr. Assistant Professor

Mrs. K. S. L. Lavanya, Sr. Assistant Professor

HOD/EEE

Dr. J. Siva Vara Prasad

LBRCE IEEE Student Branch- STB64751
National Science Day-28th February 2025

REPORT ON “POSTER PRESENTATION COMPETITION”

Event Type : Poster Presentation Competition

Topic/Theme : *Renewable Energy & Sustainability*

Date/Duration : 28-02-2025, 10.00AM to 12.30 PM

Name of Coordinator : Mr.K.Nagalinga Chary

Target Audience : All B.Tech Students of EEE,LBRCE

Total no. of Participants : 13

Judges : Dr.M.S.Giridhar and Mr.P.Deepak Reddy

Objective of the event :

- (i) To present complex ideas, research findings, or innovations in a visually appealing and concise manner.
- (ii) To develop skills in organizing information clearly with an engaging design.
- (iii) To help in learning and understanding complex topics through visual representation.
- (iv) To showcase new ideas, projects, or solutions that can contribute to advancements in a particular field.

Description/Report on Event:

LBRCE IEEE Student Branch of our college in association with Green Energy Club of EEE Department is conducted an Poster Presentation Competition on “*Renewable Energy & Sustainability*” on the occasion of “**National Science Day-2025**”

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National Science Day 2025 is a momentous occasion to celebrate scientific achievements, honour great scientists, and inspire future generations to engage in research and innovation. Observed every year on **February 28th**, National Science Day commemorates the discovery of the **Raman Effect** by Sir C.V. Raman, a milestone in the field of physics that earned him the **Nobel Prize in Physics in 1930**.

LIST OF PARTICIPANTS:

S.No	Roll Number	Name of the Student	Year
1	23761A0246	Shaik Arshiya	2 nd
2	23761A0212	G.Goetha	2 nd
3	23761A0216	J.Ganesh	2 nd
4	23761A0223	M.Renu Sri	2 nd
5	23761A0205	Vaishnavi Bogadi	2 nd
6	24765A0209	P. Sri Harshitha	2 nd
7	23765A0224	Md.Hussain	3 rd
8	23761A02A7	Sk. Sameer	2 nd
9	23765A0206	K.Chandrika	3 rd
10	23765A0213	P.Anusha	3 rd
11	23761A02A1	R.Phanindra	2 nd
12	23761A0249	S.Navya	2 nd
13	24765A0210	P.Mohan Kishore	2 nd

WINNERS LIST:

S.No.	Roll Number	Name of the Student	Year/Branch	Prize
1	23761A0205	Vaishnavi Bogadi	II/EEE	1
2	24765A0210	P.Mohan Kishore	II/EEE	2
3	23765A0206	K.Chandrika	III/EEE	3

Photographs:

 <p>LAKIREDDY BALI REDDY COLLEGE OF ENGINEERING (An Autonomous Institution since 2016)</p> <p>Approved by AICTE, New Delhi and Government of AP through JNTU, Hyderabad L.R. Reddy Nagar, Mylavaram, AP, India. Pincode - 521201</p> <p>LBRCE IEEE Student Branch IEEE 9543 Green Energy Club Sponsored</p> <p>Poster Presentation Competition</p> <p>in "Renewable Energy & Sustainability" on the occasion of National Science Day 28-30-2025</p>  	 <p>Participant delivering the topic</p>
 <p>Participant delivering the topic</p>	 <p>Participant delivering the topic</p>
 <p>RENEWABLE ENERGY & SUSTAINABILITY Poster</p>	 <p>Participant delivering the topic</p>

Publications for the A.Y: 2024-25

1. K. Harinadha Reddy, “Adaptive extreme learning machine using soft computing fuzzy propositions—Validating operating state of solar energy system”, Applied Soft Computing, Volume 164, Issue C, 111966, October 2024, ISSN: 1568-4946
<https://doi.org/10.1016/j.asoc.2024.111966>.

Q1 Journal

2. Y.Raghuvamsi, K.Teeparthi, Vinod Kumar D.M, Imran Abdul, P.Srihari, “Distribution system state estimation using physics-guided deep learning approach”, Electric Power Systems Research, Volume 236, 110922, July, 2024, ISSN: 0378-7796,
<https://doi.org/10.1016/j.epsr.2024.110922>,

3. Y.Raghuvamsi, Sreenadh Batchu, Kiran Teeparthi, Topology and FDIA Identification in Distribution System State Estimation using a Data-Driven Approach, Measurement, 253,Part D, PP.117741, May 2025, ISSN:0263-2241
<https://doi.org/10.1016/j.measurement.2025.117741>

Q1 Journal

4. G. Tabita, P. Umapathi Reddy, “Design of interline hybrid continuous trans-impedance turns ratio controlled tuning power filter: New approach to suppress harmonics in energy system,” Electric Power Systems Research, Volume 238, 111107,

October, 2024, ISSN 0378-7796,
<https://doi.org/10.1016/j.epsr.2024.111107>.

5. G. Tabita, P. Umapathi Reddy “Interline hybrid continuous converter impedance control-based tuning filter: effective harmonic suppression in DG-integrated electrical power grid”, *Electrical Engineering*, 107, pp. 5501–5510, Nov, 2024, ISSN: 0948-7921,
<https://doi.org/10.1007/s00202-024-02815-9>,

6. Varaprasad Janamala, P. Sobha Rani, K. Radha Rani, K. Swarnasri, “Transformative impact of electrical engineering on society, education, academia, and industry: a brief review” *Electrical Engineering*, <https://doi.org/10.1007/s00202-025-03066-y>, April 2025,